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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/483,321	01/14/2000	Gary L. Swoboda	TI-28937	8221

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EXAMINER

DAY, HERNG DER

ART UNIT	PAPER NUMBER
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2123

DATE MAILED: 06/20/2003

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Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action

Application No.

09/483,321

Applicant(s)

SWOBODA, GARY L.

Examiner

Samarina Makhdoom

Art Unit

2123

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 16 May 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.
- ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☐ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
 - (b) ☐ they raise the issue of new matter (see Note below);
 - (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 - (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____

3. ☐ Applicant's reply has overcome the following rejection(s): _____.
4. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☒ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: _____

Claim(s) objected to: _____

Claim(s) rejected: 1-4.

Claim(s) withdrawn from consideration: _____

8. ☐ The proposed drawing correction filed on _____ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____
10. ☐ Other: _____

Russell Frejd
RUSSELL FREJD
PRIMARY EXAMINER

Continuation of 5. does NOT place the application in condition for allowance because: Bhattacharya discloses a serial signal having the first logic state, See the Abstract for the disclosure of a test access port for communication to a boundary-scan architecture. See Figure 13, and corresponding text in Col. 11, lines 31, et Seq. for the disclosure of several logic states 1001-1 to 1001-n, therefore the boundary-scan architecture has a first logic state. The opposite start bit is not recited in Claim 4. Claim 4 states "a serial signal having said first logic state..." and "...a start bit having a second logic state opposite to said first logic state..." therefore the logic state applies to the serial signal not the start bit. It is unclear how a start bit is opposite. A bit can be either '1' or '0'; the logic state is applied to the serial signal. Claim 4 supplies a serial signal to a selected module of registers; Bhattacharya discloses a serial signal applied to a grouping (module) of registers (See, Figures 11 and 12, Figure 11 shows a grouping of registers including the user register, control register, and bypass register as part of the Snoopy Access Port. Figure 12 shows the state diagram of the Snoopy Access Port with the first and second logic states, and the serial signals between them. See text corresponding to Figures 11-12 in Col. 9, line 46 et Seq.